MSCCS–08/MSCCS-202/ MCA-202

December – Examination 2020

MSCCS (Final)/MCA (II Year) Examination

Computer Architecture and Microprocessors

Paper : MSCCS-08/MSCCS-202/MCA-202

Time : 2 Hours]   [*Maximum Marks : 80*

Note :– The question paper is divided into two Sections A and B. Write answers as per the given instructions.

Section–A   8×2=16

(Very Short Answer Type Questions)

Note :– Answer all questions. As per the nature of the question delimit your answer in one word, one sentence or maximum upto **30** words. Each question carries 2 marks.

1. (i) What is Subroutine ?

(ii) Define the term opcode.

(iii) What is function of address bus ?

(iv) Draw a diagram of crossbar interconnection structure.

(v) Define the term stack.

(vi) What is Random Access Memory ?

(vii) What are status signals of 8085 ?

(viii) What do you understand by I/O ports ?

Section–B   4×16=64

(Short Answer Type Questions)

Note :– Answer any *four* questions. Answer should not exceed **200** words. Each question carries 16 marks.

2. Explain the data transfer instruction with suitable examples.

3. What is Macro ? How is it differ from subroutine ?

4. Explain the programmed I/O method of I/O interfacing.

5. What is assembly language ? How is it differ from machine language ?

6. Define Memory. Explain memory hierarchy used in computer system.

7. Define Control Unit. Explain hardwired control unit method of control unit designing.

8. Define Microprocessor. Differentiate the microprocessor and microcontroller.

9. Explain CISC architecture with suitable diagram.